

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR § 1.53(b))

Attorney Docket No.	1875.2120003
First Inventor	Janardhanan S. AJIT
Title	I/O Circuit Using Low Voltage Transistors Which Can Tolerate High Voltages Even When Power Supplies are Powered Off
Express Mail Label No.	

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO:

 Commissioner for Patents
 Mail Stop Patent Application
 P.O. Box 1450
 Alexandria VA 22313-1450

1. ☐ Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original, and a duplicate for fee processing)
2. ☐ Applicant claims small entity status.
See 37 CFR 1.27.
3. ☒ Specification [Total Pages 26]
(preferred arrangement set forth below)
 - Descriptive title of the Invention
 - Cross Reference to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to sequence listing, a table,
 or a computer program listing appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
4. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets 27]
5. Oath or Declaration [Total Pages]
 a. ☐ Newly executed (original or copy)
 b. ☐ Copy from a prior application (37 CFR 1.63(d))
 (for continuation/divisional with Box 18 completed)
 i. ☐ **DELETION OF INVENTOR(S)**
 Signed statement attached deleting inventor(s)
 named in the prior application, see 37 CFR §§
 1.63(d)(2) and 1.33(b).
6. ☒ Application Data Sheet. (See 37 CFR 1.76).

7. ☐ CD-ROM or CD-R in duplicate, large table or Computer Program
(Appendix)
8. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
 a. ☐ Computer Readable Form (CRF)
 b. Specification Sequence Listing on:
 i. ☐ CD-ROM or CD-R (2 copies); or
 ii. ☐ paper
 c. ☐ Statements verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

9. ☐ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(b) Statement ☐ Power of Attorney
(when there is an assignee)
11. ☐ English Translation Document (if applicable)
12. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
13. ☐ Preliminary Amendment
14. ☒ Two (2) Return Receipt Postcards (MPEP 503)
(Should be specifically itemized)
15. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
16. ☐ Nonpublication Request under 35 U.S.C. 122 (b)(2)(B)(i). Applicant must attach form PTO/SB/35 or its equivalent.
17. ☒ Other: Authorization Under 37 C.F.R. § 1.136(a)(3)
☐ Other: The name of the assignee is: _____

18. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

☐ Continuation ☐ Divisional ☒ Continuation-in-Part (CIP) of prior application No: 10 / 043,788
 Prior application information: Examiner LAM Group/Art Unit: 2816

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

19. CORRESPONDENCE ADDRESS

☒ Customer Number or Bar Code Label
 Customer No. 26111



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PATENT TRADEMARK OFFICE

or ☐ Correspondence address below

NAME	STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.		
	Attorneys at Law		
NAME (Print/Type)	Jeffrey T. Helvey	Registration No. (Attorney/Agent)	44,757
SIGNATURE	<u>Jeff Helvey</u>	Date	July 14, 2003

This collection of information is required by 37 CFR 1.33(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of:

Janardhanan S. AJIT

Appl. No.: To Be Assigned

Filed: Herewith

For: **I/O Circuit Using Low Voltage
Transistors Which Can Tolerate High
Voltages Even When Power Supplies**

Confirmation No.: To Be Assigned

Art Unit: To Be Assigned

Examiner: To Be Assigned

Atty. Docket: 1875.2120003

**Authorization To Treat A Reply As Incorporating An Extension Of
Time Under 37 C.F.R. § 1.136(a)(3)**

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

The U.S. Patent and Trademark Office is hereby authorized to treat any concurrent or future reply that requires a petition for an extension of time under this paragraph for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. The U.S. Patent and Trademark Office is hereby authorized to charge all required extension of time fees to our Deposit Account No. 19-0036, if such fees are not otherwise provided for in such reply.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

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Date: July 14, 2003

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July 14, 2003

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Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Mail Stop: Patent Application

Re: U.S. Non-Provisional Utility Patent Application Under 37 C.F.R. § 1.53(b)
(Continuation-in-Part of Appl. No. 10/043,788; Filed: January 9, 2002)
Appl. No. (To Be Assigned); Filed: Herewith
For: **I/O Circuit Using Low Voltage Transistors Which Can Tolerate High
Voltages Even When Power Supplies are Powered Off**
Inventor: Janardhanan S. AJIT
Our Ref: 1875.2120003

Sir:

The following documents are forwarded herewith for appropriate action by the U.S. Patent and Trademark Office:

1. USPTO Utility Patent Application Transmittal Form PTO/SB/05;
2. Authorization to Treat a Reply As Incorporating An Extension of Time 37 C.F.R. § 1.136(a)(3);
3. U.S. Utility Patent Application entitled:

**I/O Circuit Using Low Voltage Transistors Which Can Tolerate High Voltages
Even When Power Supplies are Powered Off**

and naming as inventor:

Janardhanan S. AJIT

the application comprising:

- a. an Application Data Sheet (37 C.F.R. § 1.76);
 - b. A specification containing:
 - i. 22 pages of description prior to the claims;
 - ii. 3 pages of claims (7 claims - 2 Independent);
 - iii. a one (1) page abstract;
 - c. 27 sheets of drawings: (Figures 1-8, 9A-9C, 10, 11A-11D, 12-22); and
4. Two (2) return postcards.

This patent application is being submitted under 37 C.F.R. § 1.53(b) without Declaration and without filing fee. Correspondence should be sent to Customer Number 26111.

It is respectfully requested that, of the two attached postcards, one be stamped with the filing date of these documents and returned to our courier, and the other, prepaid postcard, be stamped with the filing date and unofficial application number and returned as soon as possible.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Jeffrey T. Helvey
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Enclosures

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